# DENON

Hi-Fi Personal Component System

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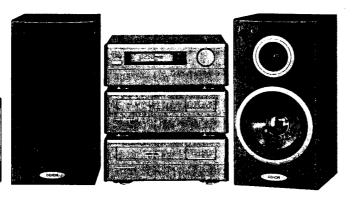
# **SERVICE MANUAL**

## PERSONAL COMPONENT SYSTEM

UNIT No. UDRA-70 (MW LW FM Stereo Receiver)

UNIT No. UDR-70 (Cassette Tape Deck)
UNIT No. UCD-70 (Compact Disc Player)

D70 Aho



The D-70 Personal Component System consists of the following:

	1
Receiver Section	UDRA-70
Remote Control Unit	RC-142
Cassette Deck Section	UDR-70
CD Section	UCD-70
Speaker Section	USC-70
	ı

## MAIN FEATURES

- 30 FM/AM station random preset tuner
  - Random presetting makes for easy operation and will come in handy when the FM stations increases in the future.
- Power amplifier designed for quality sound
- \* 30W + 30W high quality power amplifier.
- SDB control
  - \* Super dynamic bass control for clear low bass sound.
- Super linear converter and high performance digital filter
  - \* DENON's unique systems for preventing loss of CD sound quality create an excellent sound field.
- · Editing circuit included
  - \* When performing edited recording onto tapes, tracks can be selected automatically so that the blank space on the tape is minimum.
- Dolby B, C N.R
  - \* For playback and recording with high quality sound.
- CD SRS circuit
  - \* CDs can be recorded at the touch of a button.
- · Easy-to-use remote control unit

Check that the following parts are included in the package aside from the main unit:

 ① Operating Instructions
 1

 ② FM Antenna
 1

 ③ AM Loop Antenna
 1

 ④ Remote Control Unit
 1

 ⑤ R6P/AA batteries
 2

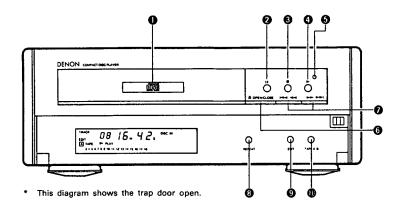
 ⑥ System Connector
 2

 ⑦ FM Antenna adapter
 1

NIPPON COLUMBIA CO., LTD.

# PART NAMES AND FUNCTIONS

CD PLAYER



- - The disc tray opens forward when the OPEN/CLOSE button () is
  - To close the disc tray, press the OPEN/CLOSE button 6 again.
- II PAUSE button

Press this button to stop playback temporarily. Press the PLAY button to resume playback.

- **STOP button** 
  - Press this button to stop playback.
- **▶** PLAY button

Press this button to start playing the disc. If pressed when the disc holder is open, the disc holder closes and playback begins.

Play indicator

This lights when the disc is played and sound is output.

**OPEN/CLOSE** button

Press this button to open and close the disc holder.

Press once to open the disc holder forward, then press again to

0 I◀ ◀ (automatic/manual search reverse button)
Press this button to move the pickup back to the beginning of the

desired track Press in the play or pause mode to move back a number of tracks

equal to the number of times the button is pressed.

>> | (automatic/manual search forward button) Press this button to move the pickup forward to the beginning of the desired track.

Press in the play or pause mode to move forward a number of tracks

- equal to the number of times the button is pressed.

  \* The automatic search function is set if the button is released within 0.5 seconds, and the manual search function is set if the button is held in for more than 0.5 seconds.
- REPEAT button

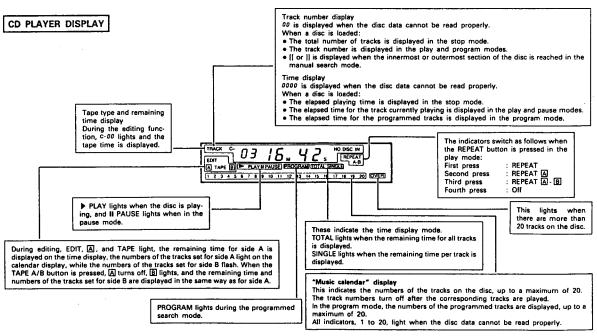
Press this button for repeat playback.

**EDIT** button

Press this button for edited recording (dividing the tracks to be recorded to fit onto sides A and B of a tape according to the tape's length).

0 TAPE A/B button

Press this button during editing to switch the display between the display for side A and the display for side B of the tape.



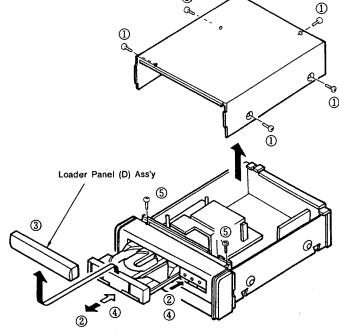
. NO DISC lights on the display if no disc is loaded, or if the disc is loaded upside-down or is heavily scratched or dirty.

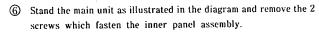
## REMOVAL OF EACH SECTION

(Follow this procedure in the reverse order when assembling.)

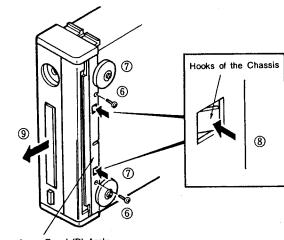
#### 1. Removal of the top cover

- 1 Remove the 5 screws which fasten the top cover.
- ② Press the ▲ OPEN/CLOSE button and eject the CD tray.
- Remove the loader panel (D) assembly in the direction of the arrow.
- ⊕ Press the ♠ OPEN/CLOSE button and retract the CD tray.
- (5) Remove the 2 screws which fasten the front panel assembly. At this time, remove with care the connector which connects the main unit assembly and the front panel side assembly.



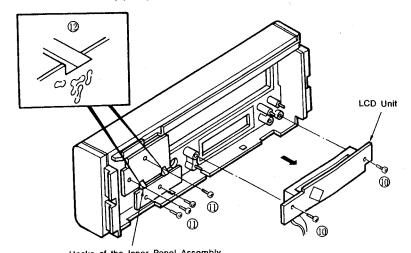


- Slightly loosen the screws of the 2 front legs.
- Remove the hooks of the chassis from the inner panel assembly.
- (9) Remove the front panel assembly in the direction of the arrow.



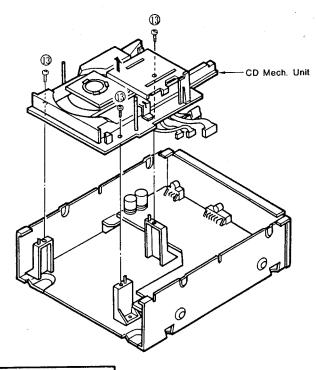
# 2. Removal of the printed wiring board assembly LCD UNIT (1U-2280-2)

- Remove the 2 screws which fasten the LCD unit.
- Remove the 4 screws which fasten the various boards.
- 12 Remove the hooks of the inner panel assembly.



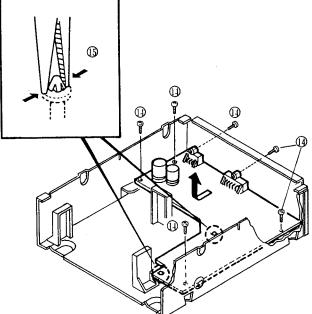
## 3. Removal of the CD mechanism unit

Remove the 3 screws which fasten the CD mechanism unit. At this time, remove with care the connector which connects the CD mechanism unit and the main unit assembly.



## MAIN UNIT ASSEMBLY (1U-2280-1)

- Remove the 6 screws which fastens the main unit assembly.
- $\ensuremath{\mbox{\footnote{$\odot$}}}$  Use radio pliers or another suitable tool to remove the 2 PCB holders which fasten the main unit assembly.

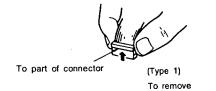


#### 

- Hold the top part of the connector, release the lock, and pull out the wires.
- When the lock of the top has not come off, the wires will not come out, so check that the lock has come off.

#### Connecting the connectors (Type 1)

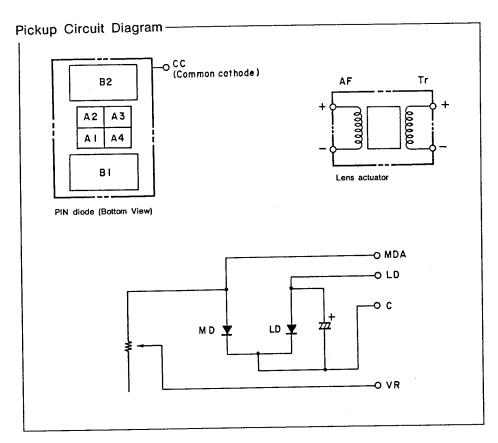
Press the top of the connector and lock it to the base, fix the bend of
the wires, match the polarities, and press in. When the top section is
not locked, the wires will come out, so check that the top section is
locked.



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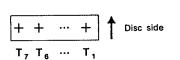
# LASER PICKUP

## Connections Diagram



#### 1. PD connector

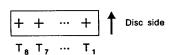
PH pin post 7 pins (Type number B7B-PH-K-S manufactured by Nippon Atchaku Tanshi Hanbai K.K.)



Tn	1	2	3	4	5	6	7
Item	A <sub>3</sub>	A <sub>4</sub>	A <sub>2</sub>	Aı	сс	B <sub>1</sub>	B <sub>2</sub>

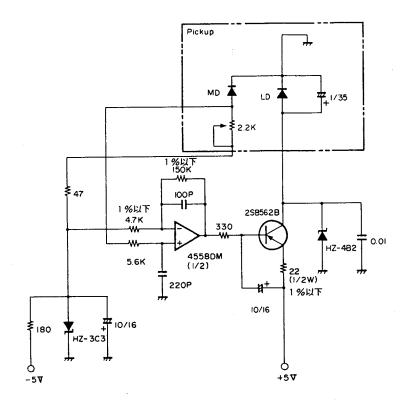
## 2. LD actuator connector

PH pin post 8 pins (Type number B8B-PH-K-S manufactured by Nippon Atchaku Tanshi Hanbai K.K.)

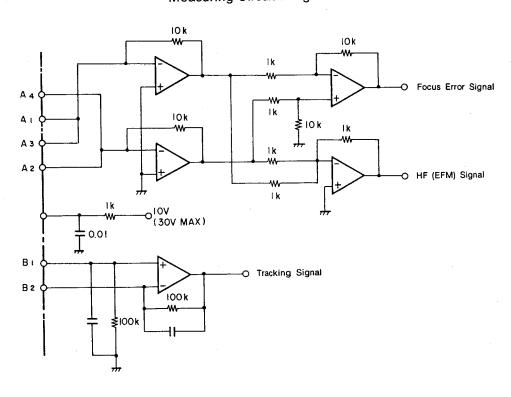


Tn	1	2	3	4	5	6	7	8
Item	С	LD	MDA	VR	TR+	TR-	AF-	AF+

## Basic Laser Drive Circuit Diagram



## Measuring Circuit Diagram



#### Precautions in Use

Read the following carefully before handling.

#### 1. Laser control circuit

The light output of the laser diode (LD) is greatly affected by temperature, so a built-in monitor photodiode should be used in the LD to supplement the light output.

In order to get rid of the dispersion of the monitor photodiode, the semiconductor resistor accompanying the pickup has been adjusted so that the mirror surface level of the HF signal becomes 250 mV when the measurement circuit of this manual and the basic laser drive circuit are used. When designing a new laser drive circuit, note that the life of the laser will be shortened when the mirror level of the HF signal becomes 275 mV with this measuring circuit.

#### 2. Wiring

Be sure to use the specified connectors for the wiring.

Note that the eye pattern may deteriorate when there is a microprocessor or other digital noise source in the vicinity from the photodiode to the harness.

Note that a poor connection related to the LD and actuator connector will cause deterioration of the laser, and so there should not be any looseness of connectors.

#### Precautions in Handling

This mechanism has been precisely assembled and adjusted at a special factory. It should not be disassembled or adjusted without good reason. Pay attention to the following points related to handling.

#### 1. General items

- (1) Storage
  - Avoid storage in places with high temperatures and high humidity, and in places exposed to a lot of dust.
- (2) Handling

The unit has been precisely adjusted and care should be taken so as not to expose the unit to shocks through dropping or careless handling.

- 2. Semiconductor laser (LD)
- (1) Protection of the eyes from the laser

The output of the LD is via an objective lens and is a maximum of 400  $\mu$ W, but reaches approximately  $1.3 \times 10^{4}$ W/cm<sub>2</sub> in places where there is condensed light. After being condensed by the objective lens, the beam widens and so is all right at a distance of 30 cm or further, but during operation the LD should never be allowed to be viewed directly or through another lens or mirror since this is dangerous.

(2) Destruction by surge currents or static electricity

When a large current flows through the LD, even for a very short period, the strong light which the LD generates itself will advance the deterioration of the LD or destroy it.

Wire a switch into the LD drive circuit or provide another method of preventing the flow of surge currents. Also, when handled without care, the LD can be destroyed instantly by the application of static electricity from the body. Therefore, when handling the LD, be sure to ground your body and ground the measuring instruments, jigs, and tools. It is also desirable to use a grounding mat on the work bench and floor.

#### 3. Lens actuator

- (1) The actuator section uses a strong magnetic circuit, so that when magnetic bodies come too close, their characteristics are altered.

  Also be careful not to allow foreign matter to enter from the cover gap.
- (2) Lens cleaning

Dust or dirt adhering to the objective lens will change the performance.

To clean, blow the dirt away with clean air from an air blower.

#### 4. Handling

Be sure not to contact the lens when handling the LD.

Note that direct contact of the body or other objects with the circuit of the LD board will cause deterioration to occur, so sufficient care should be taken.

## SERVICE POINTS

#### Parts replacement of the tray mechanism (Figs. 1 and 2)

#### (1) Removal of the tray

Open the tray and use a flat-bladed screwdriver to press the stopper portions of Fig. 1 (one each in the left and right locations) in the direction of the black arrow, then remove in the direction of the white arrow.

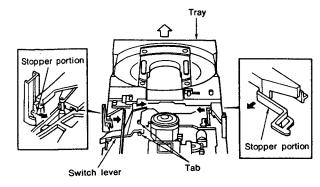


Fig. 1

### (2) Mounting of the tray (Figs. 1, 2, and 3)

Rotate the switch lever in the direction of the arrow, set the latches of the tray as illustrated in Fig. 2, then align the rails of the tray in the grooves of the loading plate, and insert so that the pinch lever pins of the switch lever enter into the rack grooves. Push in the tray while pressing the stopper portion inside a little.

(Check that the latches are in the positions illustrated in Fig. 2.)

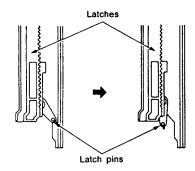


Fig. 2

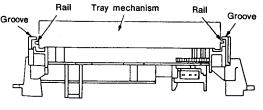


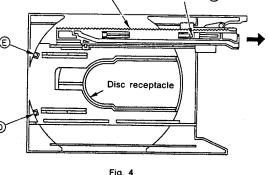
Fig. 3

# (3) Replacement of the disc holder (Fig. 4)

With the tray removed, remove tabs ① and ⑥ of the disc receptacle of Fig. 4, then lift up and off.

Latch

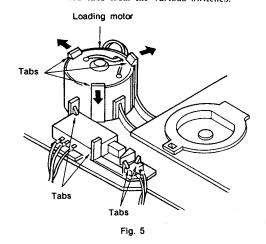
Tab ⑤



## (4) Replacement of the latches (Fig. 4)

Set the latches into the condition of Fig. 4, lift the latch tab (F) up about 1 mm with a flat-bladed screwdriver and remove the rack in the direction of the arrow.

#### (5) Removal of the loading motor and switches (Fig. 5) Remove the belt from the loading motor, then remove the 3 tabs. Remove the fixed tabs from the various switches.



- (6) Replacement of the belt
- Replace the belt with the tray removed.
- (7) Replacement of the clamper (Fig. 6)

Hook the elongated holes of the clamper onto the C arm, bend the elongated hole sections and attach.

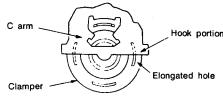
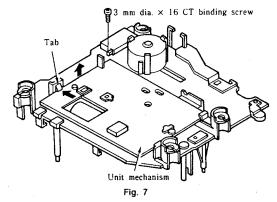


Fig. 6

(8) Replacement of the switch lever (Fig. 1) Remove the tabs of the bottom side (in 2 locations).

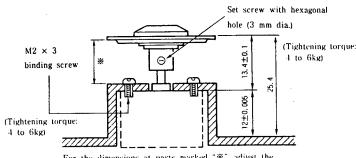
## 2. Removal of the unit mechanism (Fig. 7)

After removing the loading mechanism, remove the tab of the bottom surface (in one location) as illustrated in Fig. 7.



- (1) To replace the DC motor (D2) and the turntable, follow the procedure below
- 1) Pull the turntable (plastic) off vertically from the unit plate.
- When fitting on the servicing turntable (metal), make a height adjustment. (Fig. 8)

Do not exert excessive force to the shaft of the DC motor (D2) at this time.



For the dimensions at parts marked "\*", adjust the height using the turntable height JIG.

Fig. 8

HG No.: SGK-0030

3) At the time of service replacement of the DC motor (D2), do not apply excessive force in direction B. When part C of the unit plate is misshapen, it will cause eye pattern deterioration. (Fig. 9)

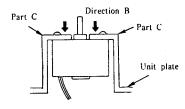
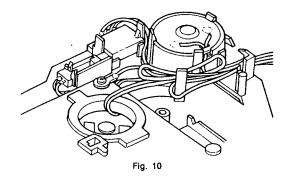


Fig. 9

#### NOTE:

- Motor replacement or turntable replacement method Remove the pressure-fitted turntable, and remove the motor screws.
- Do not reuse a turntable (plastic) that has been removed once.
- (2) When disassembling and assembling the unit mechanism, assemble with wiring resembling that of Fig. 10



#### 3. Inspection of the objective lens (Fig. 11)

Handle so as not to get dirt or dust on the objective lens of the lens actuator section. Note that when used for a long period, dirt or dust may have adhered to the objective lens. Try cleaning the surface of the objective lens with a dry, clean cotton swab.

If the dirt still does not come off, moisten the cotton swab with a small amount of water and wipe. When doing this, be careful not to get water on any parts other than the lens.

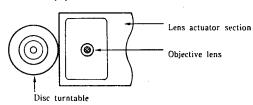


Fig. 11

## 4. Inspection for laser breakdown

The laser is normally driven with a current of 30 to 80 mA. If this laser drive current value is measured at 120 mA or higher in the circuit, the laser may be thought to be faulty. (The current value is measured by taking the voltage (0.99 to 3.3 V) across both ends of R401, which is 33 ohms).

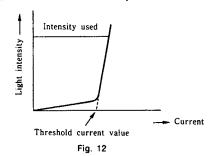
## 5. Precautions at time of servicing (Fig. 12)

#### (1) Semiconductor laser

The semiconductor laser is very susceptible to static electricity destruction and surge currents. Be careful never to touch the terminals of the semiconductor laser and the terminals of the flexible board with your hands or a tool.

As illustrated in Fig. 12, the current and light intensity characteristics increase abruptly once the threshold current value is exceeded.

Also note that this threshold current differs a little from laser to laser. In view of this, when replacing the unit mechanism or any work that involves setting the amount of light of the laser, be sure to turn the adjustment control VR401 fully in the counterclockwise direction, and then raise it to the specified value.



#### (2) Handing the unit mechanism (Fig. 13)

When handling the pickup mechanism and the unit mechanism, use a ground ring such as the one illustrated in Fig. 13. (A ground ring can be constructed using ordinary lead wire.)

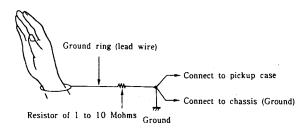


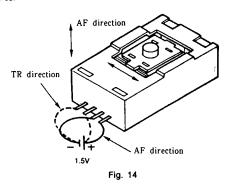
Fig. 13

## CD SECTION

#### 6. Inspection of the actuator (Fig. 14)

Check the resistance value of the actuator coil. It is normal if the values are as follows:

If the coils are open or shorted, the actuator may be thought to be broken. Also, a 1.5 V battery can be used to observe if the lens moves



## ADJUSTMENT METHOD

The microprocessor contained in this unit incorporates a service program which allows a wide variety of service adjustments to be conducted easily by using the operation buttons.

#### 1. Method of starting the service program

NOTE: When the service program starts, normal operations are not longer possible with the operation buttons.

## 2. Operation functions when the service program is operating

Operation button	Operation function	Description
◆ OPEN/CLOSE	Opens and closes the disc holder.	<ul> <li>Opening and closing takes place when the rotation of the disc has stopped.</li> <li>Other operation buttons are performed when the opening and closing operation is completed.</li> </ul>
■ STOP	Stops system operation.	<ul> <li>Track number display becomes 0 !</li> <li>Press when an adjustment has been completed or is redone.</li> </ul>
▶ PLAY	Operates the focus servo and rotates the disc.	<ul> <li>Press at the time of the tracking offset adjustment.</li> <li>After the operation is completed, the track number display becomes</li> <li>D2 .</li> </ul>
II PAUSE	Operates the focus servo, tracking servo, slide servo, and the spindle servo.	<ul> <li>When the play button has been pressed, the tracking servo and slide servo are operated.</li> <li>After the operation is completed the track number display becomes D3.</li> </ul>
Other buttons	Operation is not normal.	<ul> <li>Do not operate buttons other than the above.</li> <li>When a button is operated by mistake, immediately turn the power switch off.</li> </ul>

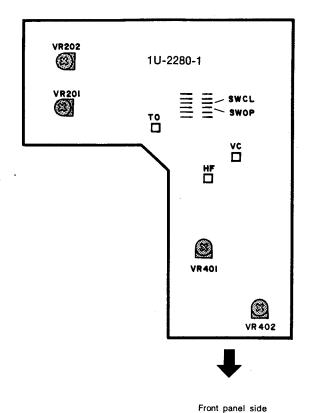
NOTE: Do not use the remote control while the service program is operating.

## 3. Adjustment method

- (1) Measuring instruments required in the adjustment
  - ① DC voltmeter
  - ② Oscilloscope

## Outline Diagram of Adjustment Locations

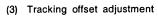
## 1U-2280A-1 CD Unit (Component Side)

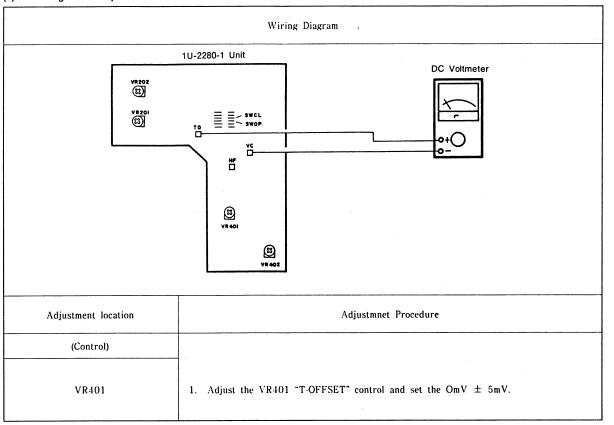


NOTE: VR201 and 202 are adjusted at the factory before shipping and there is no need to adjust.

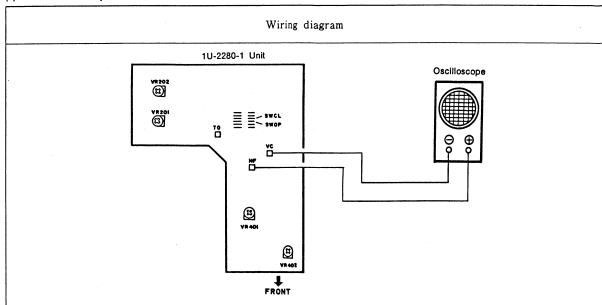
#### (2) Adjustment preparation

1.	Set the adjustment control (VR401, 402) to the position illustrated.	VR401 (T-OFFSET)
		VR402 (F-OFFSET)
2.	Adjustment step	Tracking offset     Focus offset





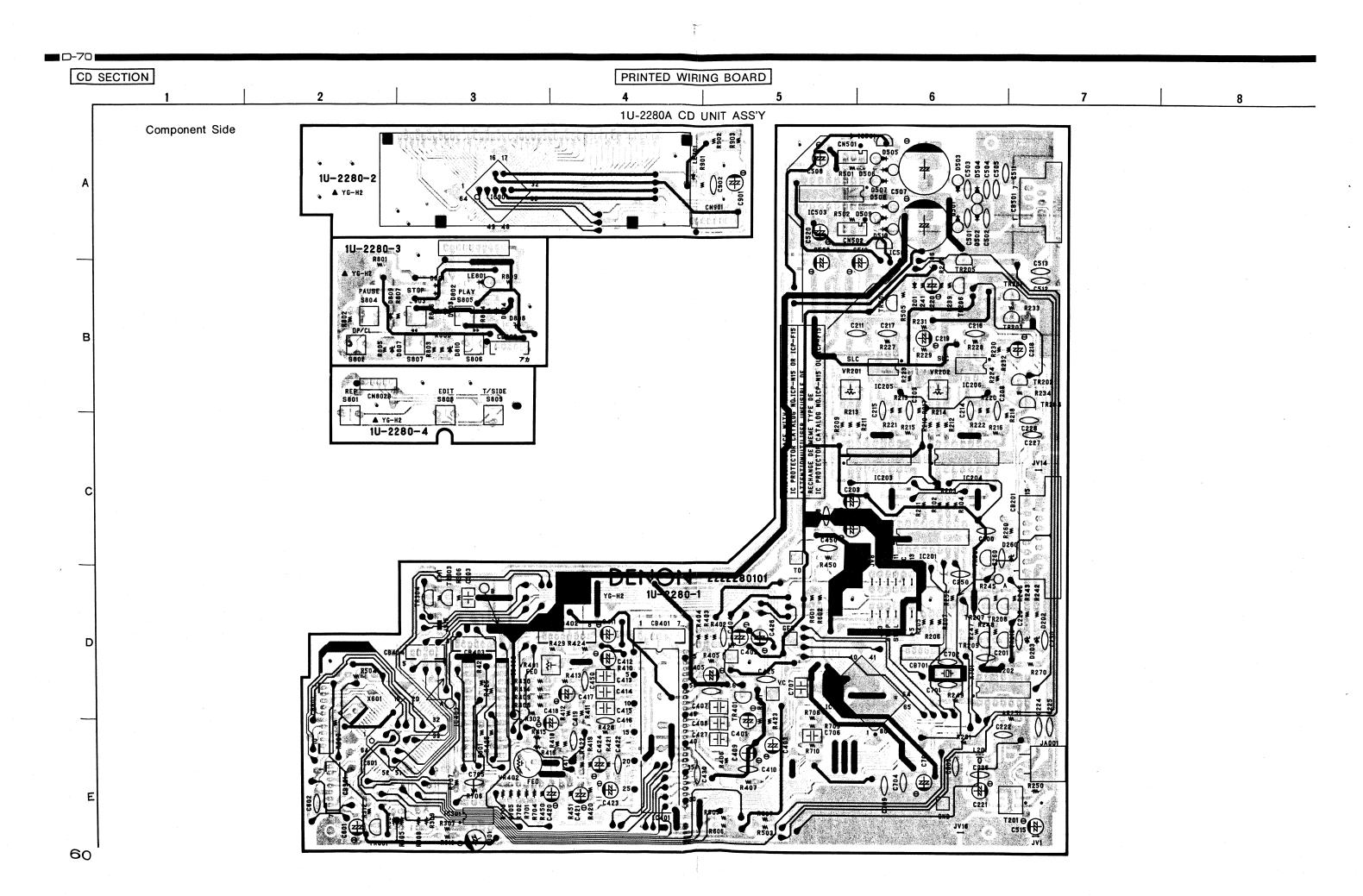
## (4) Focus offset Adjustment



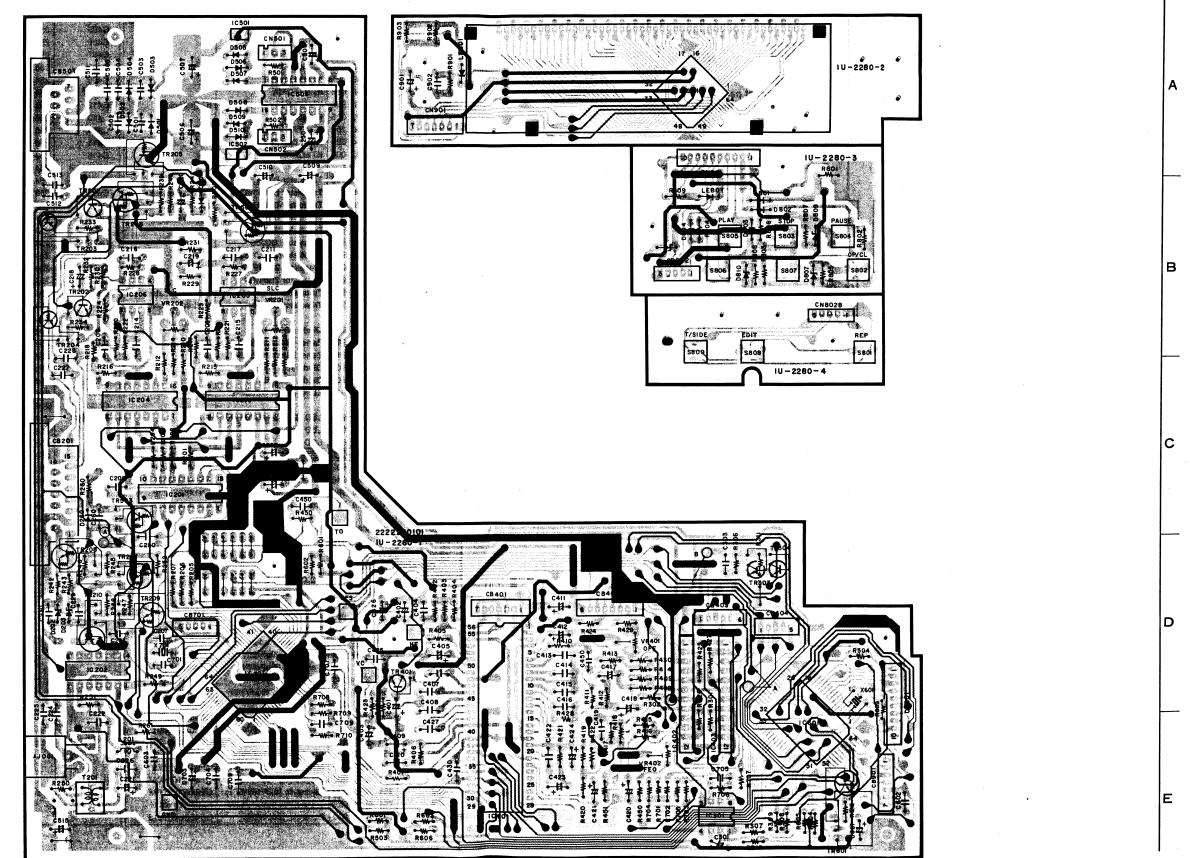
Oscill	oscope	Adjustment location	Check items		
V	V H Control Oscilloscope				
50mV/div or 20mV/div	0.2 μ/div or 0.5 μ/div	VR402	EFM waveform Adjust for Fine waveform		
			EFM waveform		

Adjustment Procedure

- 1. Press the PLAY button.
- 2. Adjust VR402 ("F-OFFSET") so that the eye pattern jitter is minimum.



Pattern Side



D-70

## CD SECTION

## NOTES ON THE PARTS TABLE

#### NOTE FOR PARTS LIST

- Part indicated with the mark "@" are not always in stock and possibly to take a long period of time for supplying, or in some case supplying of part may be refused.
- When ordering of part, clearly indicate "1" and "I" (i) to avoid mis-supplying.
- Ordering part without stating its part number can not be supplied.
  Part indicated with the mark "\*\pm" is not illustrated in the exploded view.
- Not including Carbon Film ±5%, 1/6 W, 1/4W Type in the P. W. Board parts list.
- Parts marked with this symbol △ have critical characteristics. Use ONLY replacement parts recommended by the manufacturer.
- Refer to the following table for the codes of the resistors and capacitors appearing on the parts list.

#### Resistors

Ex.: RN 14K Type Shape and performance	Power Resi	. — ist- Allowal	FR Others
RD : Carbon RC : Fixed RS : Metallic film RW: Winding RN : Metal film RK : Metal mixture	2B: '8W 2E: '4W 2H: '2W 3A: 1W 3D: 2W 3F: 3W 3H: 5W	F:±1% G:±2% J:±5% K:±10% M:±20%	P : Pulse-resistant type NL : Low noise type NB : Non-burning type FR : Fuse resistor F : Lead wire forming

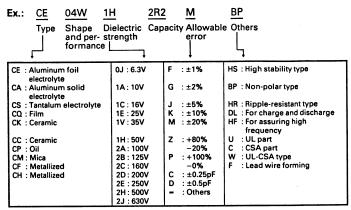
★ Resistance

1 8 2 ⇔ 1800 ohm = 1.8 kohm
Indicates number of ze Indicates number of zeros after effective number

-2-digit effective number, decimal point indicated by R.

Units: ohm

#### • Capacitors



★ Capacity

2 R 2 ⇒ 2.2 μF

1-digit effective number, decimal point indicated by R.

2-digit effective number, decimal point indicated by R.

Units: μF, (for P, pF (μ μF)

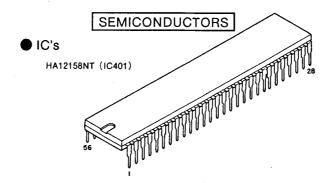
When the dielectric strength is indicated in AC, "AC" is included after the dielectric

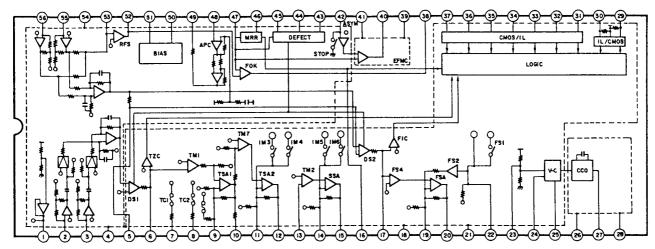
## **1U-2280A CD UNIT PARTS LIST**

	<u></u>	Anistisi			r	T	
Ref. No.	Part No.	Part Name	Remarks	Ref. No.	Part No.	Part Name	Remarks
	DUCTORS GRO			C410	253 9030 060	BC Ceramic 0.01 µF/25V	CK45=1E103K
IC201	262 1450 008 262 1409 004	IC SM5840CP		C405	254 4252 006	Electrolytic 10 µF/16V	CE04W1C100M
IC203,204	263 0712 009	IC PCM61P-L IC :RC4558P		C406 C407	253 9031 069 256 1034 018	BC Ceramic 2700pF/25V	CK45=1E272K
IC205,206	263 0712 009	IC :RC4558P		C407	255 1120 042	Metalized 0.033 µF/50V Film 0.0022 µF/50V	CF93A1H333J
IC401	263 0749 001	IC HA12158NT		C409	254 4260 032	Electrolytic 0.47 µF/50V	CQ93M1H222J CE04W1HR47M
IC402,403	263 0750 003	IC BA6290A		C411	254 4254 006	Electrolytic 10 µF/16V	CE04W1C100M
IC501,502	268 0073 905	IC ICP-N15	IC Protector	C412	254 4260 032	Electrolytic 0.47 µF/50V	CE04W1HR47M
IC503	263 0693 005	IC M5290P		C413	256 1034 047	Metalized 0.056 µF/50V	CF93A1H563J
IC601	262 1456 206	IC µPD75116GF-E16-3BE	μ-Com	C414	256 1034 005	Metalized 0.027 µF/50V	CF93A1H273J
IC701	262 1514 009	IC CXD2500AQ		C415	256 1034 005	Metalized 0.15 µF/50V	CF93A1H154J
IC901	263 0533 000	IC LC7582		C416	253 1179 071	Ceramic 390pF/50V	CK45B1H391K (DD-3)
				C417	254 4252 019	Electrolytic 22 µF/16V	CE04W1C220M
TR203,204	269 0073 908	Transistor DTC314TS	built in Resistor	C418	254 4260 016	Electrolytic 0.22 µF/50V	CE04W1HR22M
TR205	269 0046 906	Transistor DTA114ES	built in Resistor	C419	253 1004 007	Ceramic 1000pF/50V	CK45B1H102K
TR206~209	269 0020 906	Transistor DTC114ES	built in Resistor	C420	254 4250 026	Electrolytic 100 µF/6.3V	CE04W0J101M
TR210	269 0040 902	Transistor DTC144ES	built in Resistor	C421	254 4254 006	Electrolytic 10 µF/16V	CE04W1C100M
TR303	274 0144 907	Transistor :BC368		C422	253 1063 006	Ceramic 5600pF/50V	CK45B1H562K
TR304	272 0101 902	Transistor :BC369		C423	254 4260 045	Electrolytic 1 µ F/50V	CE04W1H010M
TR401	271 0102 924	Transistor 2SA1015 (GR)		C424	254 4260 016	Electrolytic 0.22 µF/50V	CE04W1HR22M
TR501	274 0136 009 272 0093 007	Transistor 2SD1913		C425	253 9030 060	BC Ceramic 0.01 µF/25V	CK45=1E103K
TR502 TR503,504	269 0020 906	Transistor 2SB1274 Transistor DTC114ES	built in Resistor	C426 C427	254 4525 024 255 1121 026	Electrolytic 47 µF/10V	CE04W1A470J
TR801	269 0046 906	Transistor DTA114ES	built in Resistor	C427	253 1121 026	Film 0.015 µF/50V Ceramic 1000pF/50V	CQ93M1H153J CK45B1H102K
INOU	203 0040 300	Transistor DIATIFES	built in riesistor	C450	253 1004 007	Ceramic 1000pF/50V	CK45B1H102K
D201	276 0432 903	Diode 1SS270A		C506,507	254 4255 704	Electrolytic 3300 µF/16V	CE04W1C332MC
D202,203	276 0462 915	Zener Diode HZS6B-2	6V	C508	254 4260 003	Electrolytic 0.1 µF/50V	CE04W10002MC
D260	276 0462 915	Zener Diode HZS6B-2	6V	C509,510	254 4254 051	Electrolytic 220 µF/16V	CE04W1C221M
D501~510	276 0550 908	Diode 1SR139-200	·	C513	253 9036 006	BC Ceramic 0.1 µF/25V	CK45=1E104Z
D801~804	276 0432 903	Diode 1SS270A		C520	254 4260 045	Electrolytic 1 µF/50V	CE04W1H010M
D807~810	276 0462 915	Zener Diode HZS6B-2	6V	C601	254 4250 026	Electrolytic 100 µF/6.3V	CE04W0J101M
				C602	253 1024 003	Ceramic 0.01 µF/50V	CK45F1H103Z
LE801	393 9483 902	LED (SLR-34MC70F120)	Green	C602,603	253 9036 006	BC Ceramic 0.1 µF/25V	CK45=1E104Z
LE901	393 9493 002	LED Ass'y		C701	253 3596 005	Ceramic 3pF/50V	CC45SL1H030C
LC901	393 4105 007	LCD Ass'y	1/4M/ Tune	C702	253 4342 041	Ceramic 5pF/50V	CC45SL1H050C
		included Carbon Film ±5% or to the Schematic Diagram		C703	254 4250 039	Electrolytic 220 µF/6.3V	CE04W0J221J
R302 R408	245 2369 902 245 2370 946	Metal Film 8.2kohm 1% 1/4W Metal Film 33kohm 1% 1/4W	RN14K2E822F	C704	253 9031 027	BC Ceramic 0.1 µF/25V	CK45=1E104K
R409	245 2370 946	Metal Film 33kohin 1% 1/4W	RN14K2E333F RN14K2E223F	C705 C706	253 9030 060 256 1034 034	BC Ceramic 0.01 µF/25V	CK45=1E103K
R707	245 2370 946	Metal Film 33kohm 1% 1/4W	RN14K2E333F	C700	255 1120 026	Metalized 0.047 µF/50V Film 0.0015 µF/50V	CF93A1H473J
"""	240 2010 340	Mictai Filin Cokolini Fili 1744	1111141220001	C709	253 9031 027	BC Ceramic 0.1 µF/25V	CQ93M1H152J CK45=1E104K
VR201,202	211 6087 928	Semi Fixed Resister 100k ohm	V06PB104	C901	254 4299 003	Electrolytic 100 µF/16V (SRE)	CE04W1C101M
VR401	211 6079 910	Semi Fixed Resister 4.7k ohm	V06PB472	C902	253 4350 004	Ceramic 680pF/50V	CC45SL1H681J
VR402	211 6064 022	Semi Fixed Resister 100k ohm	V06PB104				
CAPACIT	ORS GROUP						
C200	253 9031 027	BC Ceramic 0.1 µF/25V	CK45=1E104K				
C201	253 9036 006		CK45=1E104Z				
C202,203	254 4252 037	Electrolytic 100 µF/10V	CE04W1A101M	-			
C208,209	255 1120 000	Film 0.001 µF/50V	CQ93M1H102J				
C211	253 9031 027	BC Ceramic 0.1 µF/25V	CK45=1E104K	,			
C213	253 9031 027	BC Ceramic 0.1 µF/25V	CK45=1E104K				
C214,215 C216,217	253 3634 006 253 3627 000	Ceramic 200pF/50V Ceramic 100pF/50V	CC45SL1H201J				
C218,217	254 4254 048	,	CC45SL1H101J				
C220	254 4254 048	Electrolytic 100 µF/16V Electrolytic 220 µF/16V	CE04W1C101M CE04WIC221M				
C227,228	253 3627 000	Ceramic 100pF/50V	CC45SL1H101J	*			
C229,230	253 3643 000	Ceramic 470pF/50V	CC45SL1H471J				
C250	253 3638 002	Ceramic 300pF/50V	CC45SL1H301J				
C260	253 3643 000	Ceramic 470pF/50V	CC45SL1H471J				
C301	254 4260 061	Electrolytic 3.3 µF/50V	CE04W1H3R3M				
C303	256 1047 005	Metalized 0.022 µF/50V	CF93A1H223JH				
C401	254 4254 006	Electrolytic 10 µF/16V	CE04W1C100M				
C402	254 4299 003	Electrolytic 10 µF/16V (SRE)	CE04W1C100M				
C403	254 4250 026	Electrolytic 100 µF/6.3V	CE04W0J101M				
C404	253 3614 000	Ceramic 100pF/50V	CC45SL1H101J				

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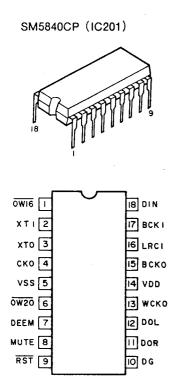
OTHER	OTHERS PARTS GROUP									
				(P.W. Board)		(1)				
	212	5606	905	Tact Swtich		9				
X601	399	0034	002	Ceramic Vibrator	CST4.00MG	1				
X701	399	0036	013	X'tal (16.9344MHz)		1				
	449	0055	302	LCD Holder		1				
CB404	205	0343	058	5P Connector Base (KR-PH)		1				
CN802	205	0321	054	5P Connector Base (RED)	Red	1				
CB403	205	0343	061	6P Connector Base (KR-PH)		1				
CB401,901	205	0343	074	7P Connector Base (KR-PH)		2				
CB801	205	0375	000	10P Connector Base (KR-PH)		1				
CB201	204	8284	022	15P System Socket		1				
CB501	204	2429	003	7P System Socket		1				
CN901	204	2312	042	7P KR-DA Connector Cord	l=240	1				
CN801	204	2225	016	10P KR-DA Connector Cord	<b>ℓ</b> =150	1				
CN802	203	8172	021	5P KR-DS Connector Cord	ℓ=70	1				
CB402	205	0343	087	8P Connector Cord (KR-PH)		1				
CN501,502	203	4564	044	3P SCN-SON Connector Cord		2				

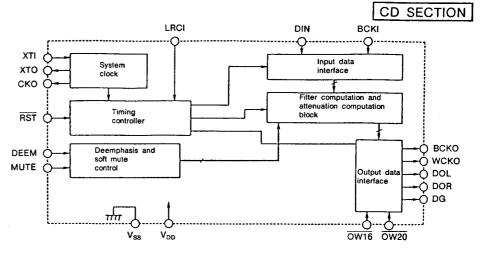




## ● Pin function table

Pin No.	Symbol	1/0	Function	Pin No.	Symbol	1/0	Function
1	VREF	0	Reference voltage output	29	COUT	0	Track count signal output
2	TRI	1	TR1 (1/V conversion amplifier) input	30	SENS	0	FZC and TZC signal output
3	TR2	ı	TR2 (1/V conversion amplifier) input	31	XRST	I	Reset signal output
4	PG	GND	Preamplifier block ground	32	DIRC	I	Direct control signal output
5	FH	0	Focus error hold signal output	33	XLT	1	Data transfer signal input
6	TE	1/0	Track error signal output, TMI input	34	DATA	I	Data signal input
7	TG1	I	TG1 switch	35	CLK	I	Data sync clock input
8	TG2	I	TG2 switch	36	LMSW	I	Limit switch input
9	TS1⊖	I	TSA1 ⊖ input	37	LDSW	[	Laser switch input
10	TS10	0	TSA1 output	38	FOK	0	FOK comparator output
11	TS2⊝	1	TSA2 ⊖ input	39	GEFM	GND	EFM comparator ground
12	TS20	0	TSA2 output	40	EFMC	0	EFM comparator output
13	TM2	I	TM2 input	41	VEFM	Vcc	EFM comparator Vcc
14	ss⊝	1	SSA ⊖ input	42	DSLC	1	Data slice level control input
15	SSO	0	SSA output	43	DFIN	t	Defect comparator input
16	MIRR	0	Mirror comparator output	44	DFO	0	Defect signal output
17	FE	1/0	Focus error signal output, FS4 input	45	DFH	0	Defect hold signal output
18	SG	GND	Servo block ground	46	MIRH	0	Error hold signal output
19	FS⊝	ı	SSA ⊖ input	47	EFMI	I	EFM signal output
20	FSO	0	FSA input	48	MD	ı	APC amplifier input
21	svcc	Vcc	Servo block Vcc	49	LD	0	APC amplifier output
22	FUD	0	Focus up/down voltage output	50	BYPS	0	Capacitor connection pin for ripple filter
23	VCR	1/0	VCO reference voltage	51	ISET	0	Reference current setting
24	PDIN	I	VCO control voltage input	52	RFO	0	RFS output
25	FRA	0	VCO free-run frequency setting	53	RF⊖	1	RFS ⊖ input
26	VVcc	Vcc	VCO Vcc	54	PVcc	Vcc	Pre-block Vcc
27	vco	0	VCO output	55	RF1	I	RF1 (I/V conversion block) input
28	VGND	GND	VCO ground	56	RF2	I	RF2 (I/V conversion block) input





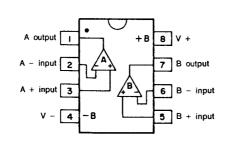
#### Pin Description

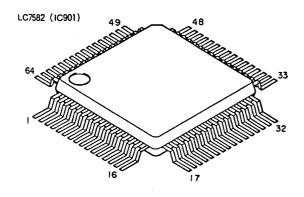
Pin number	<b>-</b>	.,							
DIP	Pin name	i/o	Function						
						OW20			
			Selection pin 1 for number of output bits	Setting	g	Н	L		
1	OW16	ip	(NOTE) NS-ON: Noise shaper on NS-OFF: Noise shaper off	OW16	Н	18bit output (NS-ON)	20bit output (NS-ON)		
				OWIG	L	16bit output (NS-ON)	18bit output (NS-ON)		
2	XTI	i	Oscillator input pin						
3	хто	0	Oscillator input pin						
4	СКО	0	Oscillator output clock (Frequency is the s	ame as XT	I)				
5	Vss	_	Ground pin						
	(N.C)								
	(N.C)								
6	OW20	ip	Selection pin 2 for number of output bits (NOTE) See the column of OW16.			s low level : 18 bis high level : 18 b			
7	DEEM	ip	Deemphasis signal input (When DEM is low level: Deemphasis is o (When DEM is high level: Deemphasis is						
8	MUTE	ip	Mute signal input (When MUTE is low level : Soft mute is off (When MUTE is high level : Soft mute is of						
9	RST	ip	System reset (Initialization)	***			***************************************		
10	DG	0	Deglitch output						
11	DOR	0	Right channel data output		-				
12	DOL	0	Left channel data output			1700			
13	WCKO	0	Output word clock						
14	V <sub>DD</sub>	_	Supply pin (5 V : Standard)						
	(N.C)								
	(N.C)								
15	ВСКО	0	Output bit clock						
16	LRCI	ip	Clock of the input data sample rate (fs)				**		
17	BCKI	ip	Input bit clock						
18	DIN	ip	Input data						

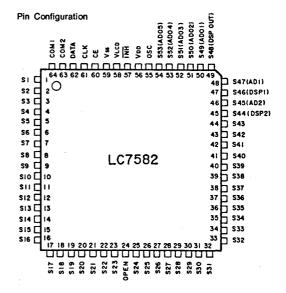
 $i: Input \ pin \qquad ip: Input \ pin \ with \ pull-up \ resistor \qquad o: Output \ pin$ 

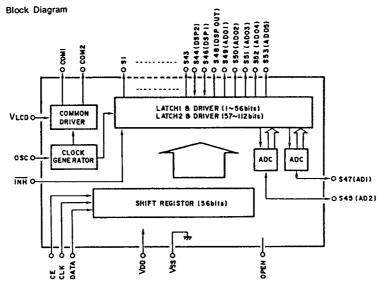
BA15218, RC4558 (IC205、206、301)











#### Pin Description

S1~S13 : Segment output pins

S46 (DSP1), S44 (DSP2) : Segment output or DSP input pins S47 (AD1), S45 (AD2) : Segment output or AD input pins S48 (DSPOUT) Segment output or DSP output pins \$49~\$53 (AD01~5) : Segment output or AD output pins

: Common output pins (At 1/1 duty, only COM1 is used and COM2 is open) COM1,2

V<sub>LCD</sub> OSC : Pin for LCD bias voltage setting

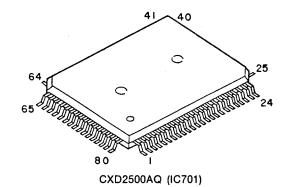
: Oscillation pin

CE, CLK, DATA : Input pins for serial data transfer

 $V_{ss},\ V_{DD}$ : Supply pins

: Display-off input pin (Valid only with the output driver. As a result, the transfer of serial data is possible while the display is off.) INII

OPEN : No connection



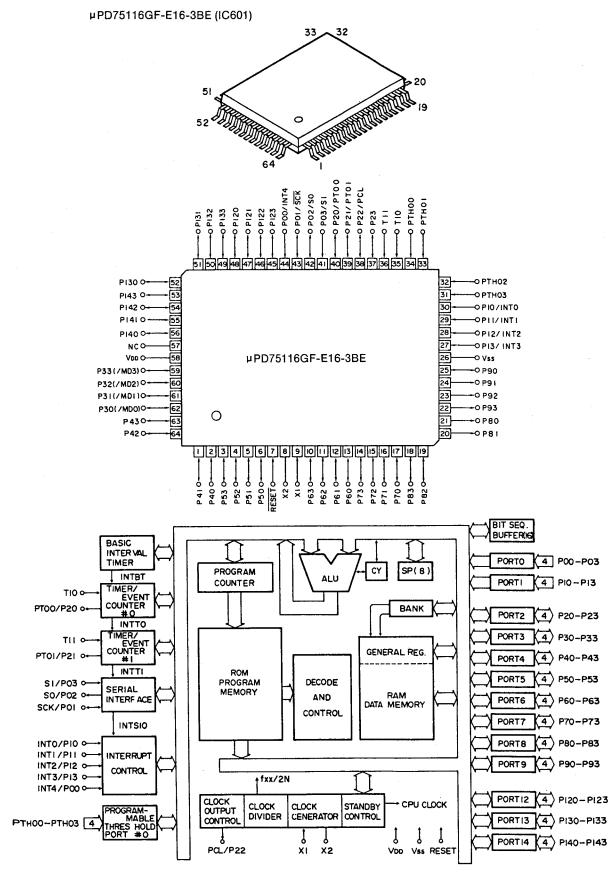
## • CXD2500AQ Pin Function Table

Pin   Now   Now   Now   Pin description		<del></del> 1	5. D2000AQ 1 111 1 UI		
2 FSW	I .	1 1	1/	0	Pin description
MON	1	FOK	I		Focus OK input pin. Used in SENS output and the servo auto sequencer.
MDP	2	FSW	0	Z.0	Output filter switching output of the spindle motor.
S MDS	3	MON	0	1.0	On-off control output of the spindle motor.
6 LOCK O 1.0 Samples GFS at 460 Hz. When GFS is "H", H is output. L is output when there is "L", 8 times in succession.  7 NC —  8 VCOO O 1.0 Oscillation circuit output for analog EFM PLL.  9 VCOI I Oscillation circuit output for analog EFM PLL. flock = 8.6436 MHz.  10 TEST I Test pin, always grounded.  11 PPO O 1.2.0 For charge pump used with analog EFM PLL.  12 Vss Ground  13 NC —  14 NC —  15 NC —  16 VPCO O 1.2.0 PLL charge pump output used for vari-pitch.  17 VCKI O Clock input fcextex from the external VCO for varipitch equals 16.9344 MHz.  18 FILO O Analog Filter output (slave = digital PLL) for master PLL.  19 FILI I Filter input for master PLL.  20 PCO O 1.2.0 Charge pump output for master PLL.  21 AVss Analog ground.  22 CLTV I VCO control voltage input for master.  23 AV <sub>100</sub> Analog supply (+5 V)  24 RF I EFM signal input  25 BIAS I Grounded  26 ASY1 I Grounded  27 ASYO O 1.0 EFM full-swing output. (L = Vss. H = V <sub>DD</sub> )  28 ASYE I Grounded  29 NC —  30 PSSL I Switching input for the audio data output mode. Serial output with "L" and parallel output with "H".  31 WDCK O 1.0 D/A interface for 48-bit slot. Word clock f = 2Fs.	4	MDP	0	1,Z,0	Servo control of the spindle motor.
NC	5	MDS	0	1,Z,0	Servo control of the spindle motor.
8 VCOO O 1.0 Oscillation circuit output for analog EFM PLL. 9 VCOI I Oscillation circuit output for analog EFM PLL. flock=8.6436 MHz. 10 TEST I Test pin. always grounded. 11 PDO O 1.Z.0 For charge pump used with analog EFM PLL. 12 Vss Ground 13 NC 14 NC 15 NC 16 VPCO O 1.Z.0 PLL charge pump output used for vari-pitch. 17 VCKI O Clock input fcexter from the external VCO for varipitch equals 16.9344 MHz. 18 FILO O Analog Filter output (slave = digital PLL) for master PLL. 19 FILI I Filter input for master PLL. 20 PCO O 1.Z.0 Charge pump output for master PLL. 21 AVss Analog ground. 22 CLTV I VCO control voltage input for master. 23 AVvv Analog supply (+5 V) 24 RF I EFM signal input 25 BIAS I Grounded 26 ASYI I Grounded 27 ASYO O 1.0 EFM full-swing output. (L = Vss. H = Vvo) 30 PSSL I Switching input for the audio data output mode. Serial output with "L" and parallel output with "H". 31 WDCK O 1.0 D/A interface for 48-bit slot. LR clock f = Fs.	6	LOCK	0	1.0	Samples GFS at 460 Hz. When GFS is "H", H is output. L is output when there is "L", 8 times in succession.
9 VCOI I Oscillation circuit output for analog EFM PLL. flock = 8.6436 MHz.  10 TEST I Test pin, always grounded.  11 PDO 0 1.Z.0 For charge pump used with analog EFM PLL.  12 Vss Ground  13 NC -	7	NC	-	-	
10 TEST I Test pin. always grounded.  11 PDO O 1.Z.0 For charge pump used with analog EFM PLL.  12 Vss Ground  13 NC —  14 NC —  15 NC —  16 VPCO O 1.Z.0 PLL charge pump output used for vari-pitch.  17 VCKI O Clock input fcenter from the external VCO for varipitch equals 16.9344 MHz.  18 FILO O Analog Filter output (slave = digital PLL) for master PLL.  19 FILI I Filter input for master PLL.  20 PCO O 1.Z.0 Charge pump output for master PLL.  21 AVss Analog ground.  22 CLTV I VCO control voltage input for master.  23 AVvo Analog supply (+5 V)  24 RF I EFM signal input  25 BIAS I Grounded  26 ASY1 I Grounded  27 ASYO O 1.0 EFM full-swing output. (L = Vss. H = Vvo)  28 ASYE I Grounded  29 NC —  30 PSSL I Switching input for the audio data output mode. Serial output with "L" and parallel output with "H".  31 WDCK O 1.0 D/A interface for 48-bit slot. Word clock f = Ps.	8	vcoo	0	1.0	Oscillation circuit output for analog EFM PLL.
11 PDO 0 1.Z.0 For charge pump used with analog EFM PLL.  12 Vss Ground  13 NC -  14 NC -  15 NC -  16 VPCO 0 1.Z.0 PLL charge pump output used for vari-pitch.  17 VCKI O Clock input fcexter from the external VCO for varipitch equals 16.9344 MHz.  18 FILO 0 Analog Filter output (slave = digital PLL) for master PLL.  19 FILI I Filter input for master PLL.  20 PCO 0 1.Z.0 Charge pump output for master PLL.  21 AVss Analog ground.  22 CLTV I VCO control voltage input for master.  23 AVvo Analog supply (+5 V)  24 RF I EFM signal input  25 BIAS I Grounded  26 ASYI I Grounded  27 ASYO 0 1.0 EFM full-swing output. (L = Vss. H = Vvd)  28 ASYE I Grounded  29 NC -  30 PSSL I Switching input for the audio data output mode. Serial output with "L" and parallel output with "H".  31 WDCK O 1.0 D/A interface for 48-bit slot. Urcl clock f = 2Fs.  32 LRCK O 1.0 D/A interface for 48-bit slot. LR clock f = Fs.	9	VCOI	I		Oscillation circuit output for analog EFM PLL. flock=8.6436 MHz.
12 Vss	10	TEST	I		Test pin, always grounded.
13 NC - 14 NC - 15 NC - 16 VPCO O 1.2.0 PLL charge pump output used for vari pitch. 17 VCKI O Clock input fcenter from the external VCO for varipitch equals 16.9344 MHz. 18 FILO O Analog Filter output (slave = digital PLL) for master PLL. 19 FILI 1 Filter input for master PLL. 20 PCO O 1.2.0 Charge pump output for master PLL. 21 AVss Analog ground. 22 CLTV I VCO control voltage input for master. 23 AVDD Analog supply (+5 V) 24 RF I EFM signal input 25 BIAS I Grounded 26 ASYI I Grounded 27 ASYO O 1.0 EFM full-swing output. (L = Vss. H = Vdd) 28 ASYE I Grounded 29 NC - 30 PSSL I Switching input for the audio data output mode. Serial output with "L" and parallel output with "H". 31 WDCK O 1.0 D/A interface for 48-bit slot. Word clock f = 2Fs. 32 LRCK O 1.0 D/A interface for 48-bit slot. LR clock f = Fs.	11	PDO	0	1.Z.0	For charge pump used with analog EFM PLL.
14 NC —  15 NC —  16 VPCO O 1.2.0 PLL charge pump output used for vari-pitch.  17 VCKI O Clock input fcenter from the external VCO for varipitch equals 16.9344 MHz.  18 FILO O Analog Filter output (slave = digital PLL) for master PLL.  19 FILI I Filter input for master PLL.  20 PCO O 1.2.0 Charge pump output for master PLL.  21 AVss Analog ground.  22 CLTV I VCO control voltage input for master.  23 AVDD Analog supply (+5 V)  24 RF I EFM signal input  25 BIAS I Grounded  26 ASYI I Grounded  27 ASYO O 1.0 EFM full-swing output. (L = Vss. H = VDD)  28 ASYE I Grounded  29 NC —  30 PSSL I Switching input for the audio data output mode. Serial output with "L" and parallel output with "H".  31 WDCK O 1.0 D/A interface for 48-bit slot. Word clock f = 2Fs.  32 LRCK O 1.0 D/A interface for 48-bit slot. LR clock f = Fs.	12	Vss			Ground
15 NC -  16 VPCO O 1.Z.0 PLL charge pump output used for vari-pitch.  17 VCKI O Clock input fcexter from the external VCO for varipitch equals 16.9344 MHz.  18 FILO O Analog Filter output (slave = digital PLL) for master PLL.  19 FILI I Filter input for master PLL.  20 PCO O 1.Z.0 Charge pump output for master PLL.  21 AVss Analog ground.  22 CLTV I VCO control voltage input for master.  23 AVDD Analog supply (+5 V)  24 RF I EFM signal input  25 BIAS I Grounded  26 ASY1 I Grounded  27 ASYO O 1.0 EFM full-swing output. (L = Vss. H = VDD)  28 ASYE I Grounded  29 NC -  30 PSSL I Switching input for the audio data output mode. Serial output with "L" and parallel output with "H".  31 WDCK O 1.0 D/A interface for 48-bit slot. Word clock f = 2Fs.  32 LRCK O 1.0 D/A interface for 48-bit slot. LR clock f = Fs.	13	NC	-	-	
16	14	NC			
17 VCKI O Clock input fcenter from the external VCO for varipitch equals 16.9344 MHz.  18 FILO O Analog Filter output (slave = digital PLL) for master PLL.  19 FILI I Filter input for master PLL.  20 PCO O 1.Z.0 Charge pump output for master PLL.  21 AVss Analog ground.  22 CLTV I VCO control voltage input for master.  23 AVob Analog supply (+5 V)  24 RF I EFM signal input  25 BIAS I Grounded  26 ASY1 I Grounded  27 ASYO O 1.0 EFM full-swing output. (L = Vss. H = Vbb)  28 ASYE I Grounded  29 NC —  30 PSSL I Switching input for the audio data output mode. Serial output with "L" and parallel output with "H".  31 WDCK O 1.0 D/A interface for 48-bit slot. Word clock f = 2Fs.  32 LRCK O 1.0 D/A interface for 48-bit slot. LR clock f = Fs.	15	NC	-	-	
FILO   O   Analog   Filter output (slave = digital PLL) for master PLL.	16	VPCO	0	1.Z.0	PLL charge pump output used for vari-pitch.
19 FILI I Filter input for master PLL. 20 PCO O 1.Z,0 Charge pump output for master PLL. 21 AVss Analog ground. 22 CLTV I VCO control voltage input for master. 23 AVod Analog supply (+5 V) 24 RF I EFM signal input 25 BIAS I Grounded 26 ASY1 I Grounded 27 ASYO O 1.0 EFM full-swing output. (L = Vss. H = Vod) 28 ASYE I Grounded 29 NC — 30 PSSL I Switching input for the audio data output mode. Serial output with "L" and parallel output with "H". 31 WDCK O 1.0 D/A interface for 48-bit slot. Word clock f = 2Fs. 32 LRCK O 1.0 D/A interface for 48-bit slot. LR clock f = Fs.	17	VCKI	0		Clock input fcenter from the external VCO for varipitch equals 16.9344 MHz.
20 PCO O 1.2.0 Charge pump output for master PLL. 21 AVss Analog ground.  22 CLTV I VCO control voltage input for master.  23 AVDD Analog supply (+5 V)  24 RF I EFM signal input  25 BIAS I Grounded  26 ASY1 I Grounded  27 ASYO O 1.0 EFM full-swing output. (L = Vss. H = VDD)  28 ASYE I Grounded  29 NC -  30 PSSL I Switching input for the audio data output mode. Serial output with "L" and parallel output with "H".  31 WDCK O 1.0 D/A interface for 48-bit slot. Word clock f = 2Fs.  32 LRCK O 1.0 D/A interface for 48-bit slot. LR clock f = Fs.	18	FILO	0	Analog	Filter output (slave = digital PLL) for master PLL.
21 AVss Analog ground.  22 CLTV I VCO control voltage input for master.  23 AVDD Analog supply (+5 V)  24 RF I EFM signal input  25 BIAS I Grounded  26 ASYI I Grounded  27 ASYO O 1.0 EFM full-swing output. (L = Vss, H = VDD)  28 ASYE I Grounded  29 NC -  30 PSSL I Switching input for the audio data output mode. Serial output with "L" and parallel output with "H".  31 WDCK O 1.0 D/A interface for 48-bit slot. Word clock f = 2Fs.  32 LRCK O 1.0 D/A interface for 48-bit slot. LR clock f = Fs.	19	FILI	I		Filter input for master PLL.
22 CLTV I VCO control voltage input for master.  23 AVDD Analog supply (+5 V)  24 RF I EFM signal input  25 BIAS I Grounded  26 ASYI I Grounded  27 ASYO O 1.0 EFM full-swing output. (L = Vss. H = VDD)  28 ASYE I Grounded  29 NC —  30 PSSL I Switching input for the audio data output mode. Serial output with "L" and parallel output with "H".  31 WDCK O 1.0 D/A interface for 48-bit slot. Word clock f = 2Fs.  32 LRCK O 1.0 D/A interface for 48-bit slot. LR clock f = Fs.	20	PCO	0	1.Z,0	Charge pump output for master PLL.
Analog supply (+5 V)  Analog supply (+5 V)  BIAS I Grounded  Grounded  The supplementary of t	21	AVss			Analog ground.
24 RF I EFM signal input  25 BIAS I Grounded  26 ASY1 I Grounded  27 ASYO O 1.0 EFM full-swing output. (L = Vss. H = Vdd)  28 ASYE I Grounded  29 NC -  30 PSSL I Switching input for the audio data output mode. Serial output with "L" and parallel output with "H".  31 WDCK O 1.0 D/A interface for 48-bit slot. Word clock f = 2Fs.  32 LRCK O 1.0 D/A interface for 48-bit slot. LR clock f = Fs.	22	CLTV	I		VCO control voltage input for master.
25 BIAS I Grounded 26 ASY1 I Grounded 27 ASYO O 1.0 EFM full-swing output. (L = Vss, H = VDD) 28 ASYE I Grounded 29 NC - 30 PSSL I Switching input for the audio data output mode. Serial output with "L" and parallel output with "H". 31 WDCK O 1.0 D/A interface for 48-bit slot. Word clock f = 2Fs. 32 LRCK O 1.0 D/A interface for 48-bit slot. LR clock f = Fs.	23	ΑVoo			Analog supply (+5 V)
26 ASY1 I Grounded  27 ASYO O 1.0 EFM full-swing output. (L = Vss, H = VDD)  28 ASYE I Grounded  29 NC -  30 PSSL I Switching input for the audio data output mode. Serial output with "L" and parallel output with "H".  31 WDCK O 1.0 D/A interface for 48-bit slot. Word clock f = 2Fs.  32 LRCK O 1.0 D/A interface for 48-bit slot. LR clock f = Fs.	24	RF	ı		EFM signal input
27 ASYO O 1.0 EFM full-swing output. (L = Vss, H = VDD)  28 ASYE I Grounded  29 NC -  30 PSSL I Switching input for the audio data output mode. Serial output with "L" and parallel output with "H".  31 WDCK O 1.0 D/A interface for 48-bit slot. Word clock f = 2Fs.  32 LRCK O 1.0 D/A interface for 48-bit slot. LR clock f = Fs.	25	BIAS	I		Grounded
28 ASYE I Grounded  29 NC -  30 PSSL I Switching input for the audio data output mode. Serial output with "L" and parallel output with "H".  31 WDCK O 1.0 D/A interface for 48-bit slot. Word clock f = 2Fs.  32 LRCK O 1.0 D/A interface for 48-bit slot. LR clock f = Fs.	26	ASY1	I		Grounded
29 NC -  30 PSSL I Switching input for the audio data output mode. Serial output with "L" and parallel output with "H".  31 WDCK O 1.0 D/A interface for 48-bit slot. Word clock f = 2Fs.  32 LRCK O 1.0 D/A interface for 48-bit slot. LR clock f = Fs.	27	ASYO	0	1.0	EFM full-swing output. ( $L = V_{SS}$ , $H = V_{DD}$ )
30 PSSL I Switching input for the audio data output mode. Serial output with "L" and parallel output with "H".  31 WDCK O 1.0 D/A interface for 48-bit slot. Word clock f = 2Fs.  32 LRCK O 1.0 D/A interface for 48-bit slot. LR clock f = Fs.	28	ASYE	I		Grounded
31 WDCK O 1.0 D/A interface for 48-bit slot. Word clock f = 2Fs.  32 LRCK O 1.0 D/A interface for 48-bit slot. LR clock f = Fs.	29	NC			
32 LRCK O 1.0 D/A interface for 48-bit slot. LR clock f = Fs.	30	PSSL	I		Switching input for the audio data output mode. Serial output with "L" and parallel output with "H".
	31	WDCK	0	1.0	D/A interface for 48-bit slot. Word clock f = 2Fs.
33 V <sub>DD</sub> Supply (+5 V)	32	LRCK	0	1.0	D/A interface for 48-bit slot. LR clock f = Fs.
	33	$V_{\text{DD}}$			Supply (+5 V)

# CD SECTION

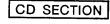
Pin no.	Pin symbol	1/	O	Pin description
34	DA16	0	1,0	DA16 (MSB) output when PSSL = 1. Serial data of the 48-bit slot when PSSL = 0. (2s' COMP, MSB first.)
35	DA15	0	1,0	DA15 output when PSSL = 1. Bit clock of the 48-bit slot when PSSL = 0.
20	DA14		1.0	DA14 output when PSSL = 1. Serial data of the 64-bit slot when PSSL = 0.
36	DA14	0	1,0	(2s' COMP, LSB first.)
37	DA13	0	1,0	DA13 output when PSSL = 1. Bit clock of the 64-bit slot when PSSL = 0.
38	DA12	0	1,0	DA12 output when PSSL = 1. LR clock of the 64-bit slot when PSSL = 0.
39	DA11	. 0	1,0	DA11 output when PSSL = 1. GTOP output when PSSL = 0.
40	DA10	0	1.0	DA10 output when PSSL = 1. XUGF output when PSSL = 0.
41	DA09	0	1,0	DA09 output when PSSL = 1. XPLCK output when PSSL = 0.
42	DA08	0	1,0	DA08 output when PSSL = 1. GFS output when PSSL = 0.
43	DA07	0	1.0	DA07 output when PSSL = 1. RFCK output when PSSL = 0.
44	DA06	0	1,0	DA06 output when PSSL = 1. C2P0 output when PSSL = 0.
45	DA05	0	1,0	DA05 output when $PSSL = 1$ . $XRAOF$ output when $PSSL = 0$ .
46	DA04	0	1,0	DA04 output when $PSSL = 1$ . $MNT3$ output when $PSSL = 0$ .
47	DA03	0	1,0	DA03 output when $PSSL = 1$ . $MNT2$ output when $PSSL = 0$ .
48	DA02	0	1,0	DA02 output when $PSSL = 1$ . MNT1 output when $PSSL = 0$ .
49	DA01	0	1,0	DA01 output when $PSSL = 1$ . MNTO output when $PSSL = 0$ .
50	APTR	0	1,0	Control output for aperture correction. "H" with Rch.
51	APTL	0	1,0	Control output for aperture correction. "H" with Lch.
52	Vss			Ground
53	IATX	l		16.9344 MHz x'tal oscillator circuit input. Or 33.8688 MHz input.
54	XTAO	0	1,0	16.9344 MHz x'tal oscillator circuit input.
55	XTSL	I		X'tal selection input pin. "L" when the x'tal is 16.9344 MHz and "H" when the x'tal is 33.8688 MHz.
56	FSTT	0	1,0	2/3 frequency division output of pins 53 and 54. Does not change with vari-pitch.
57	C4M	0	1,0	4.2336 MHz output. Changes simultaneously when varypitch is applied.
58	C16M	0	1,0	16.9344 MHz output. Changes simultaneously when varypitch is applied.
59	MD2	I		Digital Out on/off control. H when on and L when off.
60	DOUT	0	1,0	Digital-out output pin.
61	ЕМРН	0	1,0	When the playback disc has emphasis, "H" is output. "L" is output when there is no emphasis.
62	WFCK	0	1,0	WFCK (Write Frame Clock) output.
63	SCOR	0	1,0	"H" output when either sub code sync SO or S1 is detected.
64	SBSO	0	1,0	Sub P through W serial output.
65	EXCK	I		Clock input for SBSO read out use.
66	SQSO	0	1,0	SubQ 80 bit and PCM peak level data 16-bit output.
67	SQCK	I		Clock input for SQSO read-out use.
68	MUTE	I		Mute L is cancelled with H.
69	SENS	_	1,Z,0	SENS output. Output to CPU.
70	XRST	1		System set. Reset with "L".
71	DATA	ı		Serial data input from CPU.
72	XLAT	I		Latch input from CPU. Latches serial data on the fall.
73	$V_{DD}$			Supply (+5 V)
74	CLOK	I		Serial data transfer clock input from CPU.
75	SEIN	l		Sense input from SSP.
76	CNIN	0		Count signal input of number of track jumps.
77	DATO	0		Serial data output to SSP.
78	XLTO	0	1,0	Serial data latch output to SSP. Latches on the fall.
79	CLKO	0	1.0	Serial data transfer clock output to SSP.
80	MIRR	I		Mirror signal input. Used in jumps of 128 tracks or more with an auto sequencer.

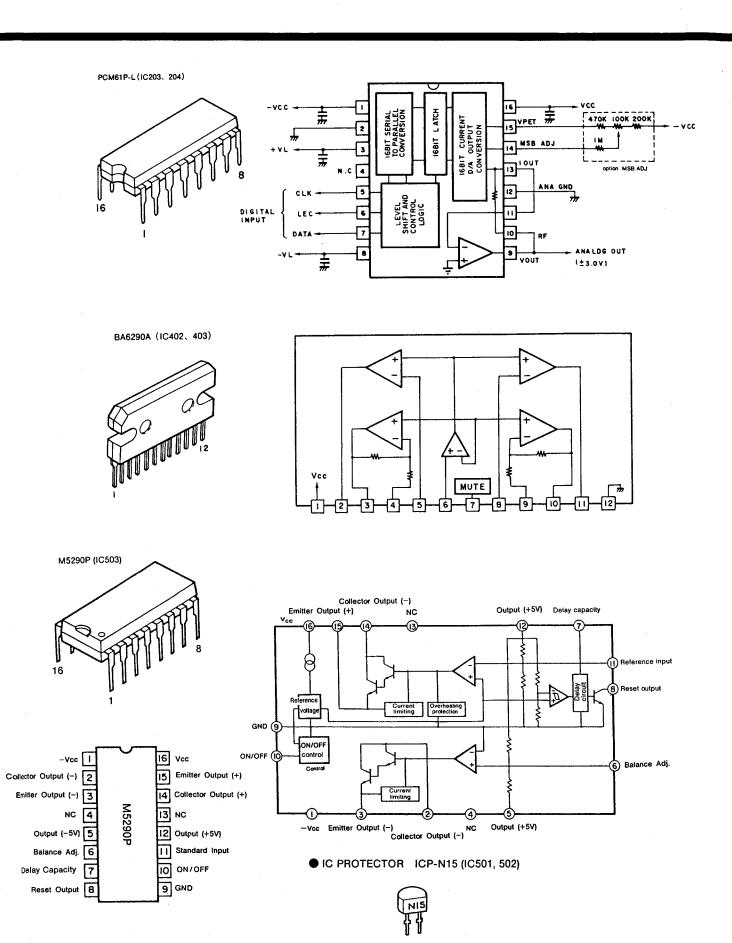
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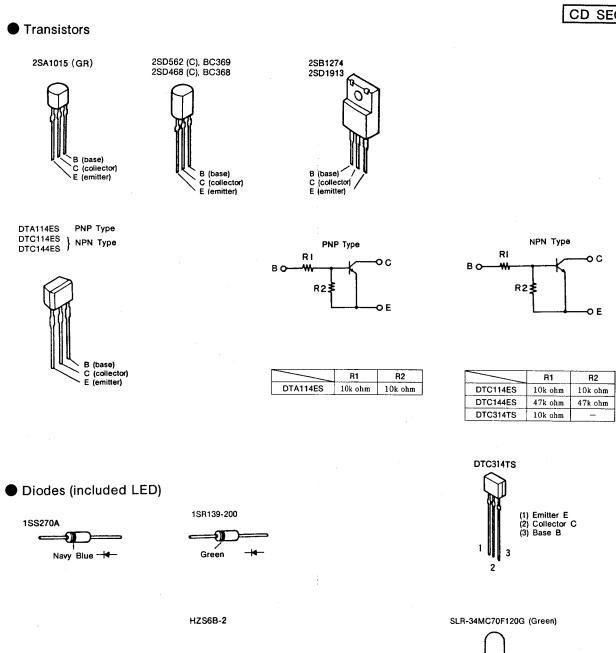


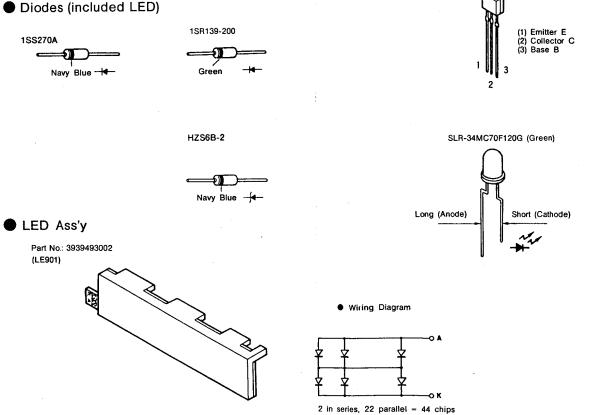
Pin	Pin name	Signal name	1/0	Active	Initial	Description	Pin	Pin name	Signal name	1/0	Active	Initial	Description
1	P41	NC	0	_	Ļ	Open	33	PTH01	PKR3	ı	Н	L	Key return
2	P40	POSPRES	0	Н	L	Reset signal (10 ms) (with vary on) for DPS	34	PTH00	PKR4	I	H	L	Key return
3	P53	NC	0	-	L	Open	35	T10	PSENSE	I	-	Н	Servo state detection signal
4	P52	NC	0	-	Ĺ	Open	36	T11	NC	ī	_	Н	Ground
5	P51	PS9	0	Н	L	Diode key scan signal	37	P23	PSVCDAT	0	_	Н	Servo control signal and data for D.F.
6	P50	PS8	0	Н	L	Momentary key scan signal 8	38	P22/PCL	PSVCLT	0	LP	Н	Servo control signal latch.
7	RESET	RESET	l			Microprocessor reset pin.	39	P21/PT01	PSVCCLK	0	-	Н	Servo control signal and clock for D.F.
8	X2	µ CONCLK	0			Microprocessor clock	40	P20/PT00	PLASER	0	L	Н	Laser diode on/off control.
9	X1	µCONCLK	I			Microprocessor clock	41	P03/SI	PSUBQ	I	_	Н	Sub code data input.
10	P63	PKS7	0	Н	L	Momentary key scan signal 7	42	P02/S0	NC	0	-	-	Open
11	P62	PKS6	0	Н	Ĺ	Momentary key scan signal 6	43	P01/SCK	PSQCK	0	-	Н	Clock for sub code reading.
12	P61	PKS5	0	Н	L	Momentary key scan signal 5	44	POO/INT4	PREM	I	11	L	Remote control signal input.
13	P60	PKS4	0	Н	L	Momentary key scan signal 4	45	P123	PDFLT	0	LP	Н	Latch signal for digital filter.
14	P73	PKS3	0	Н	L	Momentary key scan signal 3	46	P122	PAMUT	0	Н	Н	Audio mute control signal.
15	P72	PK\$2	0	Н	L	Momentary key scan signal 2	47	P121	PEMP	0	L	Н	Signal with emphasis control.
16	P71	PKS1	0	Н	L	Momentary key scan signal 1	48	P120	PDIRC	0	LP	Н	Servo control signal.
17	P70	PKS0	0	Н	L	Momentary key scan signal O	49	P133	PMVCL	0	L	Н	Disc tray drive signal.
18	P83	NC	0	-	L	Open	50	P132	PMVOP	0	L	Н	Disc tray drive signal.
19	P82	NC	0	_	L	Open	51	P131	PDMUT	0	Н	Н	Mute output for LSI.
20	P81	NC	0	_	L	Open	52	P130	PAFSO	0	_	н	Auto function serial output.
21	P80	NC	0	-	L	Open	53	P143	PFOK	i	Н	L	FOCUS OK signal input.
22	P93	PTINIT	0	Н	L	Test pin	54	P142	PSWOPN	I	L	н	Disc tray open position detection.
23	P92	PTEDIT	0	Н	L	Test pin	55	P141	PSWCLS	ī	L	L	Disc tray close position detection.
24	P91	PTSARCH	0	Н	L	Test pin	56	P140	PSWPMD	I	L	-	Pickup inner track position detection.
25	P90	PDOUT	0	Н	L	Digital output control signal	57	NC	NC				Open
26	Vss	Vss				Ground potentia! pin	58	Vdd	Vdd				Positive voltage supply pin. (+5 V)
27	P13/INT3	NC	1	-	Н	Pull-up	59	P33	PLCDOF	0	L	L	Display-off output signal for LCD driver.
28	P12/INT2	PGFS	I	Н	L	Rotation sync signal input from DPS.	60	P32	PLCDCE	0	н	L	Latch signal for LCD driver.
29	P11/INT1	PSCOR	I	↓↑	L	Sub code sync signal input.	61	P31	PLCDCLK	0	-	L	Clock for LCD driver.
30	P10/INTO	PAFSI	I	_	Н	Auto function real signal input.	62	P30	NC	0	]	L	Open
31	РТН03	PKR1	I	Н	L	Key return	63	P43	NC	0	_	L	Open
32	PTH02	PKR2	I	Н	L	Key return	64	P42	PLCDDAT	0		L	Data for LCD driver.

LP = LOW pulse



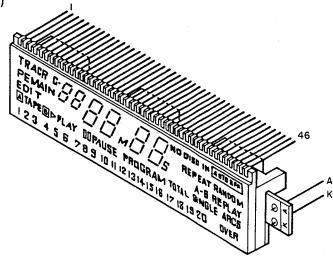






● LCD Ass'y (8155JPH)

Part No.: 3934105007

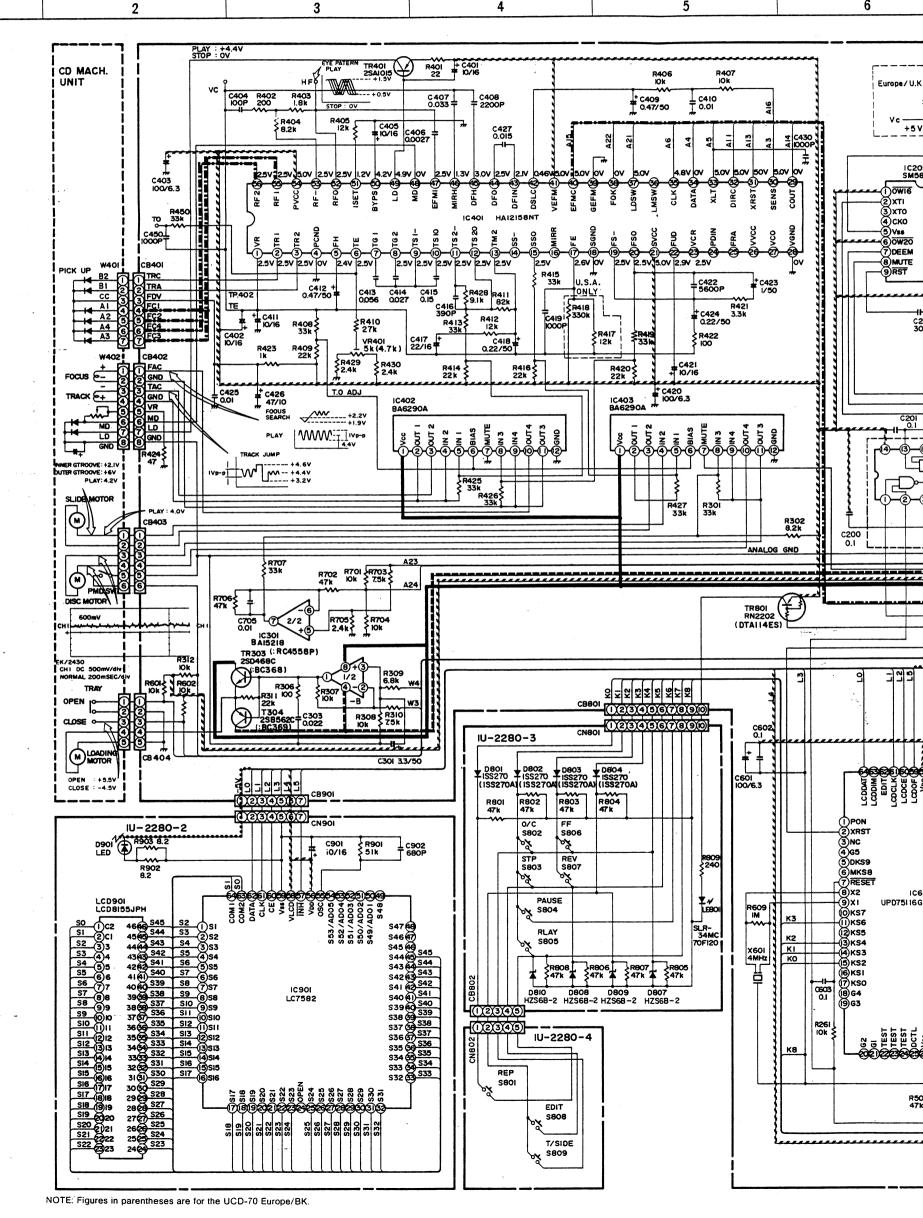


TRACK C- NO DISC IN AUTO OFF
REMAIN
EDIT

A TAPE B > PLAY DE PAUSE PROGRAM TOTAL SINGLE ARCS
1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 OVER

NO.	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
СОМ1	_	СОМ	6	PAUSE	4	В	2	TRACK	Α	C-	1f	la	1 b	2d	2a	2g	3d	Зе	3a	3ъ	4e	4f	4b
COM2	сом	-	5	PLAY	3	TAPE	1	REMAIN	EDIT	ld	le	lg	lc	2e	12	2ь	2c	3f	3g	3c	4d	4a	4g
NO.	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
СОМ1	М	TOTAL	5e	5f	5a	5c	61	6a	6b	17	В	DISC	IN	SINGLE	ARCS	RANDOM	AUTO	20	16	14	12	10	8
COM2	4c	PROGRAM	5d	5 <b>g</b>	5b	6d	6e	6g	6c	S	Α-	NO	REPEAT	18	OVER	REPLAY	OFF	19	15	13	11	9	7





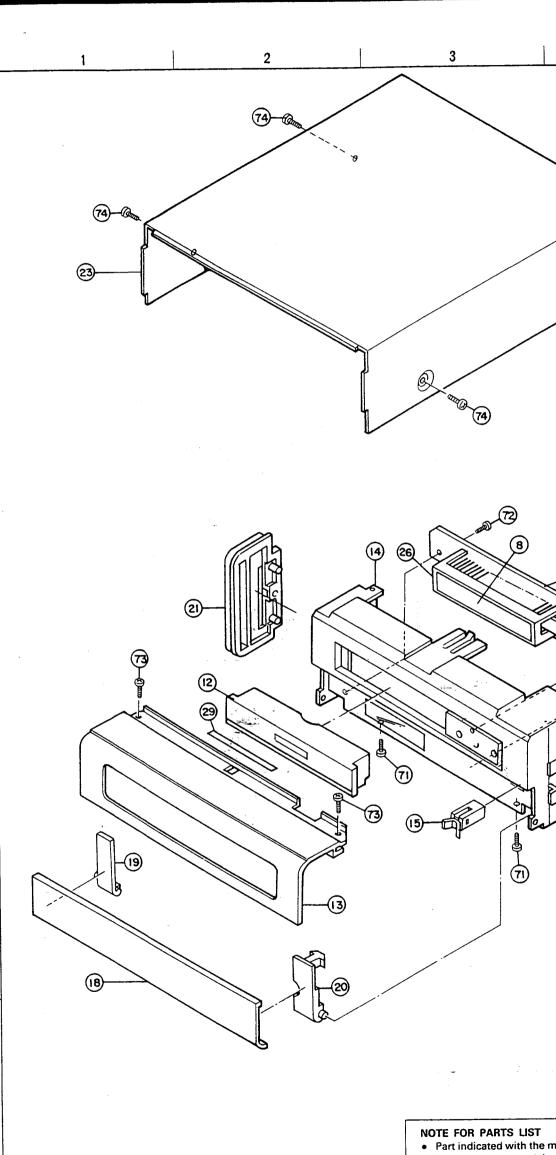
CD SECTION SCHEMATIC DIAGRAM 10 IU-2280- I SIGNAL LINE ----- -5V LINE VR20I R227 20k - +8 LINE C202 ---- -B LINE C409 0.47/50 n-vs R211 470k C217 **②DGND** R213 IM **③+**VL MSB(14) ₹R229 30k C227 4 NC (5)CLK IC205 BAI5218 (:RC4558P) டுட் 7) DATA OW16 IC203 PCM6IP-L 3)хто LRCI (Î СКО R210 VR202 TR203 DTC314 R228 20k 7)DEEM 8 MUTE R212 } 470k 2)DGND 9)RST DG(IQ +5V) 3+VL 4)NC C424 0.22/50 ③clk \_C228 \_\_I00P C250 300P IC206 BAI5218 (:RC4558P) 6)LE R206 ₹R422 100 TR204 DTC314 VO(9) C421 10/16 AI7 R260 100 A25 TR205 RN2202 (DTAI14ES) TR206 RN1202 (DTC114ES) D201 ISS270A SYSTEM CONNECTOR I D-70 ONLY R251 TXD C230 470P R252 | R245 | IOK | IOK | R301 33k DIGITAL C22I 33/16 TR209 TR210
RN1202 RN1204
(DTCI14ES) (DTCI14ES) C200 0.1 1C202 HD74HC00P TR207 # TR208 # RN1202 RN1202 (DTC114ES) P. TRANS C225 **₹R249** TR801 RN2202 (DTAIL4ES) A22 A13 MIRR CLKO XLTO DATO CNIN 1) FOK 2) FSW A23 3) MON A24 4) MDP 조 조 호 호 호 호 호 WFCK6 5 MDS 6 LOCK 7 NC 8 VCOO C16M68 تأثأر C701 ---+2.5V 9vcoi OTEST FSTT6 PLAY STOP : OV (1)PDO (2)Vss (3)NC (4)NC (5)NC (6)VPCO XTAO 64 10701 CXD2500AQ CB701 DAOLES MNTO () PON ② XRST D.MUT(1) (B) FILO 3 NC 4 G5 Ε R709 3.3k R708 6.8k (9FILI 20PCO 2)AVSS DIRC# EMP47 **5**DKS9 (6)MKS8 DA0644 DFLI 45 (B)X2 10601 LEBO R609 (0)KS7 -(1)KS6 (2)KS5 -(3)KS4 **K3** SLR-34MC LASER K2 X601 4MHz K0 (9 KS4 (4) KS3 (5) KS2 (6) KS1 (7) KS0 (603) (8) G4 (9) G3 RESET ING TR501 2SDI913(R/S)|IU-2280-5 05 D507 D506 D505 ► ► ► D807 -2 HZS68-2 KR46 ₹850I 4.7k CB501 C509 220/16 ISRI39or ISR35 ICP-NI5 R261 € 0.1/50 + F GI TEST TEST )TEST )VSS )VSS )VF )VF )GFS )SCOR )KRI C506 3300/16平 K8 D50I D502 **®®®®®®®** AC9.6V GND IC503 M5290 AC9.6V C507 3300/16 D503 D504 丁 SYSTEM CONNECTOM 2 **���������**� C513 0.1 R502 X3 ICP-NI5 3520 1/50 0508 0510 10-2280-ISRI39 or ISR35 G NOTES
ALL RESISTANCE VALUES IN OHM K=1,000 OHM M=1,000,000 OHM
ALL CAPACITANCE VALUES IN MICRO FARAD P=MICRO-MICRO FARAD
CIRCUIT AND PARTS ARE SUBJECT TO CHANGE WITHOUT PRIOR NOTICE.

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EXPLODED VIEW	OF	PΑ	RTS	LIST
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Re	f. No.	Р	art No.	.	Part Name	Remarks	Q'ty	
,	1	411	1099	139	Chassis		1	
)	2		0973	225	Rear Panel		1	
	3	104	0253	010	Foot Ass'y		4	
)	4		9016		P.C.B Holder		2	
	5	'''	_		_			
4	6	111-	2280	A	CD Unit Ass'y		1 <sup>S</sup>	Α
_	<del></del> 6-1			^	Main Unit		(1)	
Γ	6-2		_		LCD Unit		(1)	
			_		Tact Switch Unit		(1)	
	6-3				Tact Switch Unit		(1)	
_	6-4		_		-		'	
	7	200	4105	007	LCD Ass'y	LC901	1	
)	8		4105	- 1		C506,507	2	_
	9	1	4255		Chemicon 3300 µF/16V	C300,307	1	
)	10	337			CD Mech. Unit		3	
	11	103			Mech.Holder (A)		1	
	12	1	1283		Loader Panel (C) Ass'y		1 1	
	13		2134		Front Panel		11	
	14	103	1471	335	Inner Panel Ass'y		1	
	15	435	0113	009	Latch (Y3Y18)		1	_
	16	143	0742	102	Lens		1	В
	17	113	1458	119	Control Button		11	
	18	144	2133	110	Trap Door		1	
	19	401	0126	200	Door Hinge (L)		11	
•	20	401	0127	209	Door Hinge (R)		1	
	21	146			Side Panel (L) Ass'y		1	
	22		1281		Side Panel (R) Ass'y		11	
	23		0478		Top Cover	ł	11	
			1863		Rating Sheet		11	
	24				µPD75116GF-E16-3BE	μ-Com	11	
_	25	1	1456		1 1	µ-00	1	
•	26	1	0055		LCD Holder	CB501	11	
	27		2429		7P System Socket		1	
	28		4 8284		15P System Socket	CB201		
	29		2 0146		Himeron Sheet	Put on F/Panel	1	
*	30	513	3 1513	005	Laser Caution	Europe model only	1	С
*	31	513	3 0985	003	Inst. Label	Europe model only	2	
*	32	46	1 0577	068	Rubber Sheet		1	
	SCRE	ws_					Q'ty	
	71	47	3 7002	034	Tapping Screw (S) 3×6	Black	19	
	72	47	3 7500	015	Tapping Screw (P) 3×8		6	
	73	47	3 7002	021	Tapping Screw (S) 3×8	Black	2	
	74	47	3 7007	000	Tapping Screw (S) 4×8	Black	4	
	76	47	3 7500	044	Tapping Screw (P) 3×8	Black	2	
	7 <b>7</b>	47	3 7015	018	Tapping Screw (S) 3×8	Black for GND	1	
	78	42	5 0232	2 006	Adjust Washer	t0.3 Black	1	
	79	1	5 0232		1	t0.5 Clear	1	
_	PACK	ING	ACC!	ESSO	RIES (Not included EXPLO		Q'ty	
•	101		5 0154			600×600	1	
_	102	30	-	. 502	_			
•		50	3 0980	001	Cushion		2	[
•	103		1 1560				1	
•	104	1			-		18	
•	105		EN 178		Envelope Sub Ass'y	255×380	(1)	
İ	-105-1		5 0178		· ·	255/300	1	
_	105-2	1	1 222		I		(1)	
	<b>└</b> 105-3	- 1	1 226		•		(1)	
Г	<del></del> 106 ·	1	EN 178		Acc. Carton Sub Ass'y		1 <sup>S</sup>	-
	<b>⊢</b> 106-1	50	1 919	5 008	Accessory Carton		(1)	
	106-2	23	31 092	2 009	Loop Antena		(1)	
	106-3	39	5 001	9 025	FM Ant. Ass'y		(1)	l
	106-4		9 007		1		(1)	١.
<ul><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li><!--</td--><td>106-5</td><td>- 1</td><td>9 014</td><td></td><td></td><td>RC-142</td><td>(1)</td><td>1</td></li></ul>	106-5	- 1	9 014			RC-142	(1)	1
<ul><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li></li><li><!--</td--><td>106-6</td><td></td><td>04 248</td><td></td><td></td><td></td><td>(1)</td><td>i</td></li></ul>	106-6		04 248				(1)	i
<ul><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li><li>•</li></ul>	106-7	- 1	)4 631		1		(1)	1
_		1	13 138				1	1
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	108			a uu	TI THEIRIAL CALDOLL IIIII	1		1

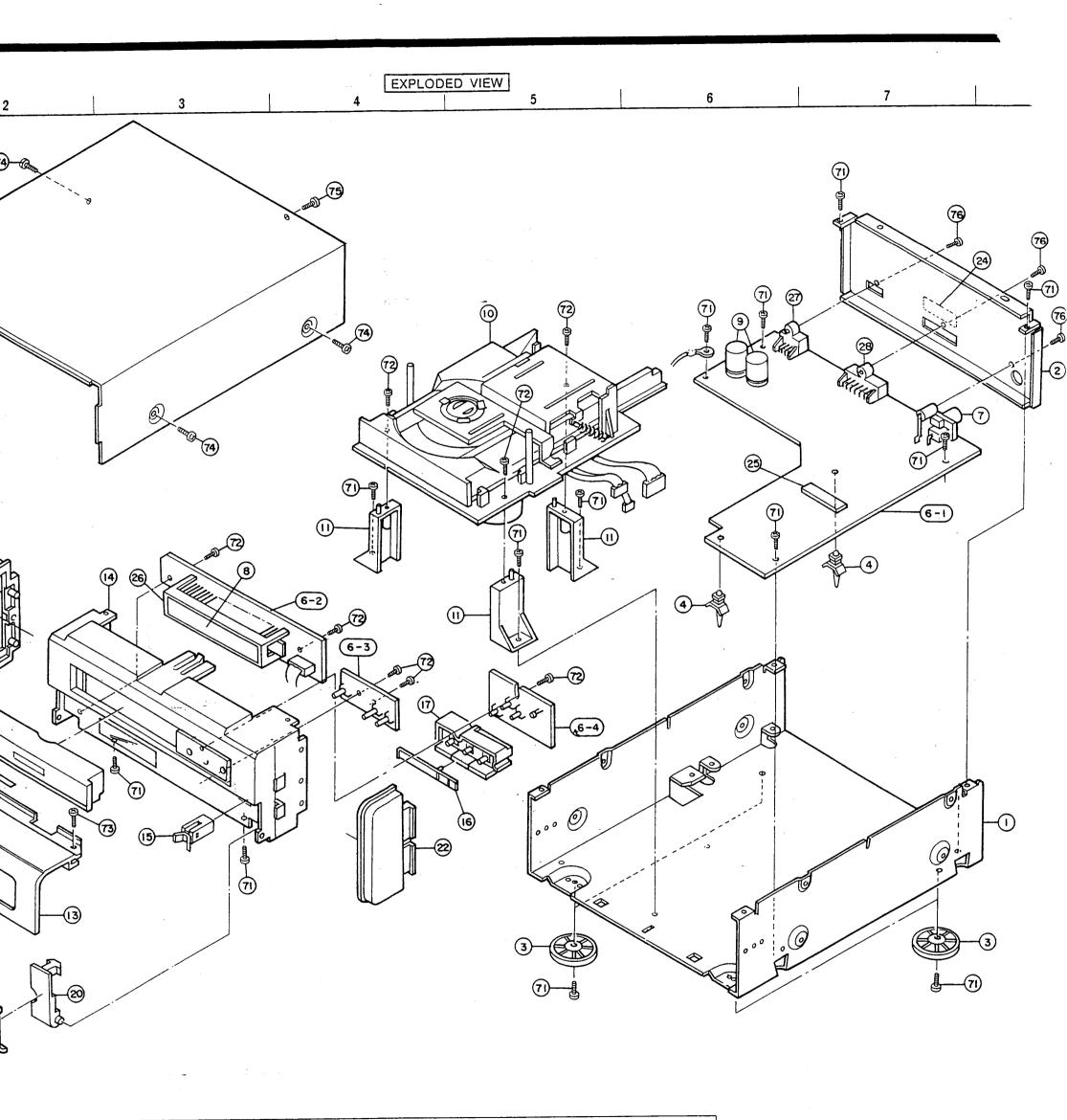
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in some case supplyingWhen ordering of part,Ordering part without sPart indicated with the

Parts marked with this syluse ONLY replacement pa

WARNING:



## NOTE FOR PARTS LIST

- Part indicated with the mark "" are not always in stock and possibly to take a long period of time for supplying, or in some case supplying of part may be refused.
- When ordering of part, clearly indicate "1" and "!" (i) to avoid mis-supplying.
- Ordering part without stating its part number can not be supplied.
  Part indicated with the mark "★" is not illustrated in the exploded view.

WARNING:

Parts marked with this symbol  $\triangle$  with have critical characteristics. Use ONLY replacement parts recommended by the manufacturer.

